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Dainippon Screen Receives 5th JPCA Award from Japan Electronics Packaging and Circuits Association

Direct Patterning System for Flexible Printed Circuits

Kyoto, Japan -- June 04, 2009 -- Dainippon Screen Mfg. Co., Ltd. has received one of the 5th JPCA Awards from the Japan Electronics Packaging and Circuits Association for its recently developed direct patterning system* for flexible printed circuits.

The 5th JPCA Awards, of which Dainippon Screen was a recipient, targeted companies making announcements at the NPI (New Product Introduction) Presentations for exhibitor products/technology seminars, organized by the Japan Electronics Packaging and Circuits Association in conjunction with its JPCA Show 2009 (39th International Electronic Circuits Exhibition). The award system was established in 2005 to provide comprehensive evaluation and public recognition of product/technical innovation, contribution to electronic circuit technology and industry expansion, and enhancement of reliability. In this the fifth year of the awards, prizes were given to six of the 23 total themes submitted.



JPCA Award presentation ceremony

Please download the photo from
<http://www.screen.co.jp/press/nr-photo/indexE.html>

Dainippon Screen plans to use the receipt of this JPCA Award as an opportunity to push forward with its goal of establishing the direct patterning system as the defacto standard for the production of flexible printed circuits. Screen also remains focused on the creation of new technologies that will contribute to the continuing expansion of the electronic circuit industry.

Comments from Kyohei Fujisawa, President of Media And Precision Technology Company

The receipt of a prestigious JPCA Award is naturally a great honor for our company. Our prize-winning direct patterning system represents a significant step forward in the manufacturing process for printed circuit boards. We are extremely happy the technical innovation and also leadership that made this possible has received such high recognition.

This is in fact the third time our company has received an award, with the FP-8500 Automatic final visual inspector for circuit wiring patterns winning in 2005 and the Sentflow Workflow system for PCB production process in 2007. This recognition is underpinned by the expectations of the printed wiring board industry for our company, and we intend to remain focused and reset ourselves for this challenge. We will of course continue with our efforts to meet the needs of our customers and also to develop next-generation technology that contributes to the expansion of the industry.

* System that prints resist patterns directly on to printed wiring boards from drawing data. This is possible thanks to a newly developed electrophotographic process that utilizes liquid toner with resist features. The system combines the resist coating, exposure, and developing operations required by conventional processing into a single operation. The result is world-beating processing capacity and a dramatically shorter circuit formation process. (See www.screen.co.jp/press/NR090416E.pdf.)